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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/848,846
	Filing Date	May 3, 2001
	First Named Inventor	Luan C. Tran
	Art Unit	2813
	Examiner Name	Laura M. Schillinger
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1689

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Satheesh K. Karra, Reg. No. 40,246 Wells St. John P.S.	
Signature	K. Satheesh Karra	
Date	April 9, 2004	

CERTIFICATE OF TRANSMISSION/MAILING			
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/848,846
Confirmation No. 1789
Filing Date May 3, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Laura M. Schillinger
Attorney's Docket No. MI22-1689
Customer No. 021567
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

RESPONSE TO MARCH 24, 2004 OFFICE ACTION

To: Mail Stop Non-Fee Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria VA 22313-1459

From: Satheesh Karra (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

Responsive to the Office Action dated March 24, 2004, Applicant amends and
remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

Amendments to the Claims are reflected in the listing of claims which begins on
page 2 of this paper.

Remarks begin on page 5 of this paper.

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